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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/685,453	10/16/2003	David B. Chung	2207/16635	5418
23838 75	90 10/13/2005		EXAMINER	
KENYON & KENYON 1500 K STREET NW			NGUYEN, HOAN C	
SUITE 700			ART UNIT	PAPER NUMBER
WASHINGTON, DC 20005			2871	

DATE MAILED: 10/13/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	<i>l)</i> /			
	10/685,453	CHUNG, DAV	ID B.			
Office Action Summary	Examiner	Art Unit				
	HOAN C. NGUYE	N 2871				
The MAILING DATE of this communication	appears on the cover s	sheet with the correspondenc	e address			
Period for Reply						
A SHORTENED STATUTORY PERIOD FOR RETHE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communication. If the period for reply specified above is less than thirty (30) days, and If NO period for reply is specified above, the maximum statutory period for reply within the set or extended period for reply will, by some and patent term adjustment. See 37 CFR 1.704(b).	ON. R 1.136(a). In no event, howevent, n. a reply within the statutory mining eriod will apply and will expire SI statute, cause the application to be	er, may a reply be timely filed num of thirty (30) days will be considered X (6) MONTHS from the mailing date of t secome ABANDONED (35 U.S.C. § 133	this communication.			
Status						
1) Responsive to communication(s) filed on 6	02 August 2005.					
2a) ☐ This action is <b>FINAL</b> . 2b) ☐ 2	☐ This action is <b>FINAL</b> . 2b)☑ This action is non-final.					
,	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice und	ler <i>Ex parte Quayle</i> , 19	935 C.D. 11, 453 O.G. 213.				
Disposition of Claims						
4)⊠ Claim(s) <u>7-14 and 21-25</u> is/are pending in	the application.					
4a) Of the above claim(s) 21-23 is/are with		ion.				
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>7-1 and 24-25</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction a	nd/or election requirem	ient.				
Application Papers						
9)☐ The specification is objected to by the Exar	miner.					
10)☐ The drawing(s) filed on is/are: a)☐	accepted or b)☐ obje	cted to by the Examiner.				
Applicant may not request that any objection to	= · ·	· ·				
Replacement drawing sheet(s) including the co						
11)☐ The oath or declaration is objected to by th	ie Examiner. Note the a	attached Office Action of for	II P10-152.			
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for for a) All b) Some * c) None of:	eign priority under 35 l	J.S.C. § 119(a)-(d) or (f).				
1. Certified copies of the priority docum						
2. Certified copies of the priority docum						
<ol> <li>Copies of the certified copies of the application from the International But</li> </ol>			mai Stage			
* See the attached detailed Office action for a						
·						
Attachment(s)						
1) Notice of References Cited (PTO-892)		nterview Summary (PTO-413)				
2) Notice of Draftsperson's Patent Drawing Review (PTO-948	·	aper No(s)/Mail Date  lotice of Informal Patent Application	(PTO-152)			
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SI Paper No(s)/Mail Date 10/16/03, 7/30/04 8/2 2005 5	7300° 6) □ C	Other:	·			

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## **DETAILED ACTION**

Applicant cancelled claims 1-6, 15-20 and added new claims 24-25.

#### Election/Restrictions

Applicant's election of Invention I, Group B (claims 7-14 and 24-25) in the reply filed on 02 August 2005 is acknowledged. Because applicant did not distinctly and specifically point out the supposed errors in the restriction requirement, the election has been treated as an election without traverse (MPEP § 818.03(a)).

Claims 21-23 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected invention and species, there being no allowable generic or linking claim. Election was made **without** traverse in the reply filed on 02 August 2005.

#### **Drawings**

The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they do not include the following reference sign(s) mentioned in the description:

- Liquid crystal material not assigning in the display structure 100 (Fig. 1)
- How is a polymer memory system 400 (Fig. 4) fitting in the display structure 100 (Fig. 1)?
- How are driving conductor 420 and data conductors 430 relating to the supply, return conductors 190/200 or addressing conductors 210/220?

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Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 1. Claims 7-8, 10-11, 13-14 and 24-25 are rejected under 35 U.S.C. 102(e) as being anticipated by Chung et al. (US 20040125272A1).

Chung et al. teach (Figs.1, 4-7) a display comprising:

## Claim 7:

a chamber to store liquid crystal material,

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a plurality of display transistors 120/320 arranged in an array across a
planar surface of the chamber, the display transistors 430 to define pixels
of the display,

a memory system 140/420 having a plurality polymer memory cells
provided within the chamber (between substrate 410 and protection layer
460) to store liquid crystal 450, each memory cell being electrical readable
and writable as Fig. 7 shown.

## Claim 13:

a reflector (mirror layer 440) coupled to one surface of the display.

# Claim 14:

- liquid crystal material provided within the chamber,
- a first control line (data or gate lines inherently) on a first surface of the
   chamber coupled to a display transistor of the plurality of display transistor
- a second control line (common electrode inherently) provided on a second surface opposite the first surface.

## wherein

## Claim 8:

the chamber includes viewable region 740 and non-viewable region 750
and the memory cells are co-located with a non-viewable region 750
thereof as Fig. 7 shown.

## Claim 9:

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 polymer memory cells are co-located with the display transistors since transistor layer 430 and the polymer memory system may be integrated into a single layer (paragraph 19).

## Claims 10 and 24:

the polymer memory system comprises: a plurality of memory cells 610, a
driving line (wording lines 620) coupled to each of the cells, and a
plurality of data lines (bitlines 630), one coupled to each of the cells.

## Claims 11 and 25:

the polymer memory system comprises a plurality of layers 1 and 2, each
layer comprising: a plurality of memory cells 610, a driving line (wording
lines 620) coupled to each of the cells in the respective layer, and a
plurality of data lines (bitlines 630), one coupled to each of the cells in the
respective layer.

# Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claim 12 is rejected under 35 U.S.C. 103(a) as being unpatentable over Chung et al. (US 20040125272A1).

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In Fig. 4, Chung et al. disclose the reflective-type LCD with mirror layers 440, thus Chung et al. does not explicitly disclose a backlight coupled to one surface of the display.

Chung et al. disclose (Fig. 5) the transmissive-type LCD with a backlight coupled to one surface of the display. Therefore, it is obvious to replace mirror layer 440 into the transparent layers and a backlight coupled to one surface of the display, the LCD changes into transmissive-type LCD.

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to further modify a display device as Chung et al. disclosed with the transparent layers replaced the mirror layers and a backlight coupled to one surface of the display for providing transmissive-type LCD.

#### Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure:

Isenberger (US 20050045930 A1) discloses <u>Polymer memory</u> having a ferroelectric <u>polymer memory</u> material with cell sizes that are asymmetric.

Andideh (US 20040217402 A1) discloses Ferroelectric devices such as ferroelectric polymer memory devices may comprise one or more layers of ferroelectric material andwiched between layers of electrodes.

Garney et al. (US 20040100828 A1) disclose Stacked memory device having shared bitlines.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to HOAN C. NGUYEN whose telephone number is (571) 272-2296. The examiner can normally be reached on MONDAY-THURSDAY:8:00AM-4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kim H. Robert can be reached on (571) 272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

HOAN C. NGUYEN Examiner Art Unit 2871

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Andrew Schechter PRIMARY EXAMINER